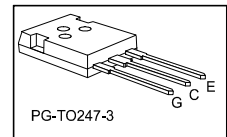
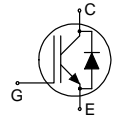


### Reverse conducting IGBT with monolithic body diode

**Features:**

- Powerful monolithic body diode with low forward voltage designed for soft commutation only
- TrenchStop® technology applications offers:
  - very tight parameter distribution
  - high ruggedness, temperature stable behavior
  - low  $V_{CEsat}$
  - easy parallel switching capability due to positive temperature coefficient in  $V_{CEsat}$
- Low EMI
- Qualified according to JEDEC J-STD-020 and JESD-022 for target applications
- Pb-free lead plating; RoHS compliant
- Complete product spectrum and PSpice Models: <http://www.infineon.com/igbt/>



**Applications:**

- Inductive cooking

Type	$V_{CE}$	$I_C$	$V_{CEsat}, T_{vj}=25^{\circ}C$	$T_{vjmax}$	Marking	Package
IHW20N120R3	1200V	20A	1.48V	175°C	H20R1203	PG-TO247-3

**Maximum ratings**

Parameter	Symbol	Value	Unit
Collector-emitter voltage	$V_{CE}$	1200	V
DC collector current, limited by $T_{vjmax}$ $T_C = 25^{\circ}C$ $T_C = 100^{\circ}C$	$I_C$	40.0 20.0	A
Pulsed collector current, $t_p$ limited by $T_{vjmax}$	$I_{Cpuls}$	60.0	A
Turn off safe operating area $V_{CE} = 1200V, T_{vj} = 175^{\circ}C$	-	60.0	A
Diode forward current, limited by $T_{vjmax}$ $T_C = 25^{\circ}C$ $T_C = 100^{\circ}C$	$I_F$	40.0 20.0	A
Diode pulsed current, $t_p$ limited by $T_{vjmax}$	$I_{Fpuls}$	60.0	A
Gate-emitter voltage Transient Gate-emitter voltage ( $t_p = 10\mu s, D < 0.010$ )	$V_{GE}$	$\pm 20$ $\pm 25$	V
Power dissipation $T_C = 25^{\circ}C$ Power dissipation $T_C = 100^{\circ}C$	$P_{tot}$	310.0 155.0	W
Operating junction temperature	$T_{vj}$	-40...+175	°C
Storage temperature	$T_{stg}$	-55...+175	°C
Soldering temperature, wavesoldering 1.6 mm (0.063 in.) from case for 10s		260	°C
Mounting torque, M3 screw Maximum of mounting processes: 3	$M$	0.6	Nm

**Thermal Resistance**

Parameter	Symbol	Conditions	Max. Value	Unit
<b>Characteristic</b>				
IGBT thermal resistance, junction - case	$R_{th(j-c)}$		0.48	K/W
Diode thermal resistance, junction - case	$R_{th(j-c)}$		0.48	K/W
Thermal resistance junction - ambient	$R_{th(j-a)}$		40	K/W

**Electrical Characteristic, at  $T_{vj} = 25^{\circ}\text{C}$ , unless otherwise specified**

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
<b>Static Characteristic</b>						
Collector-emitter breakdown voltage	$V_{(BR)CES}$	$V_{GE} = 0V, I_C = 0.50mA$	1200	-	-	V
Collector-emitter saturation voltage	$V_{CEsat}$	$V_{GE} = 15.0V, I_C = 20.0A$ $T_{vj} = 25^{\circ}\text{C}$ $T_{vj} = 125^{\circ}\text{C}$ $T_{vj} = 175^{\circ}\text{C}$	-	1.48 1.70 1.80	1.70 -	V
Diode forward voltage	$V_F$	$V_{GE} = 0V, f = 20.0A$ $T_{vj} = 25^{\circ}\text{C}$ $T_{vj} = 125^{\circ}\text{C}$ $T_{vj} = 175^{\circ}\text{C}$	-	1.55 1.70 1.80	1.75 -	V
Gate-emitter threshold voltage	$V_{GE(th)}$	$I_C = 0.50mA, V_{CE} = V_{GE}$	5.1	5.8	6.4	V
Zero gate voltage collector current	$I_{CES}$	$V_{CE} = 1200V, V_{GE} = 0V$ $T_{vj} = 25^{\circ}\text{C}$ $T_{vj} = 175^{\circ}\text{C}$	-	-	100.0 2500.0	$\mu\text{A}$
Gate-emitter leakage current	$I_{GES}$	$V_{CE} = 0V, V_{GE} = 20V$	-	-	100	nA
Transconductance	$g_{fs}$	$V_{CE} = 20V, I_C = 20.0A$	-	18.3	-	S
Integrated gate resistor	$r_G$			none		$\Omega$

**Electrical Characteristic, at  $T_{vj} = 25^{\circ}\text{C}$ , unless otherwise specified**

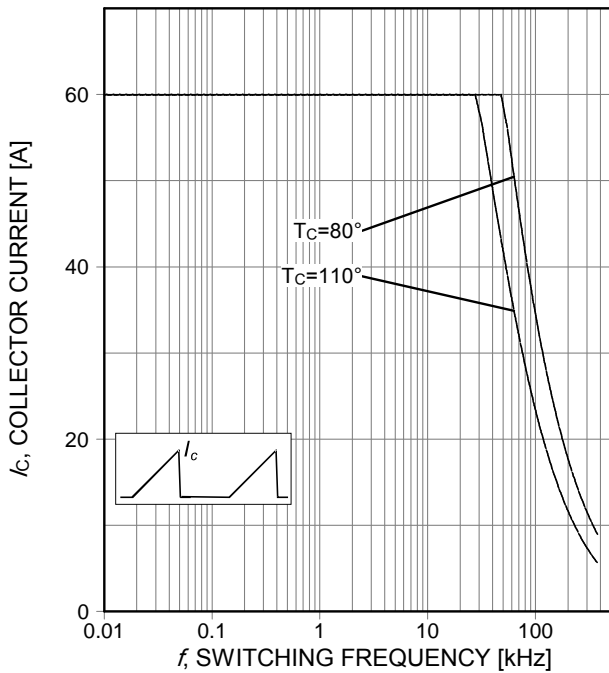
Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
<b>Dynamic Characteristic</b>						
Input capacitance	$C_{ies}$	$V_{CE} = 25V, V_{GE} = 0V, f = 1MHz$	-	1503	-	pF
Output capacitance	$C_{oes}$		-	50	-	
Reverse transfer capacitance	$C_{res}$		-	42	-	
Gate charge	$Q_G$	$V_{CC} = 960V, I_C = 20.0A,$ $V_{GE} = 15V$	-	211.0	-	nC
Internal emitter inductance measured 5mm (0.197 in.) from case	$L_E$		-	13.0	-	nH

**Switching Characteristic, Inductive Load, at  $T_{vj} = 25^{\circ}\text{C}$**

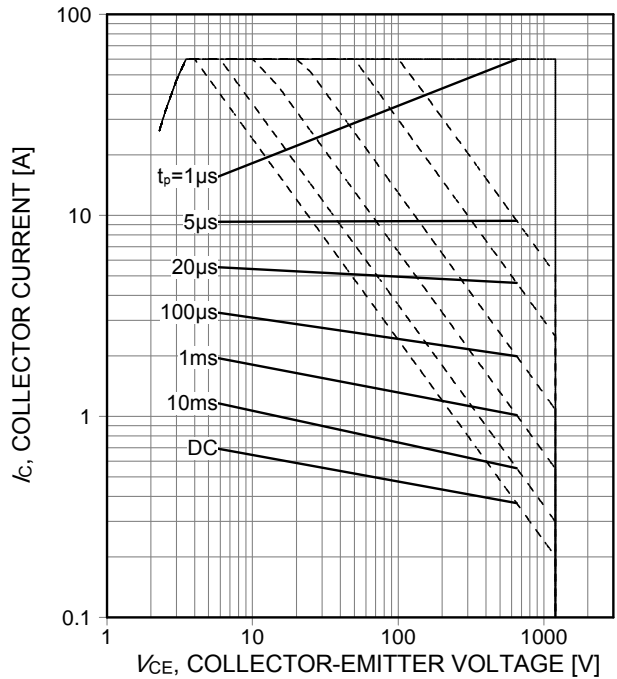
Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
<b>IGBT Characteristic</b>						
Turn-off delay time	$t_{d(off)}$	$T_{vj} = 25^{\circ}\text{C}$ , $V_{CC} = 600\text{V}$ , $I_C = 20.0\text{A}$ , $V_{GE} = 0.0/15.0\text{V}$ , $r_G = 15.0\Omega$ , $L_{\sigma} = 180\text{nH}$ , $C_{\sigma} = 39\text{pF}$ $L_{\sigma}$ , $C_{\sigma}$ from Fig. E Energy losses include "tail" and diode reverse recovery.	-	387	-	ns
Fall time	$t_f$		-	25	-	ns
Turn-off energy	$E_{off}$		-	0.95	-	mJ

**Switching Characteristic, Inductive Load, at  $T_{vj} = 175^{\circ}\text{C}$**

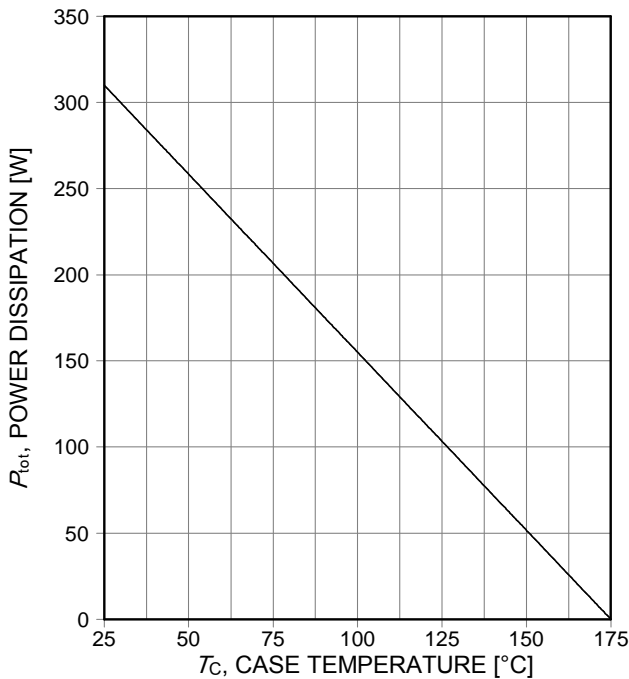
Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
<b>IGBT Characteristic</b>						
Turn-off delay time	$t_{d(off)}$	$T_{vj} = 175^{\circ}\text{C}$ , $V_{CC} = 600\text{V}$ , $I_C = 20.0\text{A}$ , $V_{GE} = 0.0/15.0\text{V}$ , $r_G = 15.0\Omega$ , $L_{\sigma} = 180\text{nH}$ , $C_{\sigma} = 39\text{pF}$ $L_{\sigma}$ , $C_{\sigma}$ from Fig. E Energy losses include "tail" and diode reverse recovery.	-	454	-	ns
Fall time	$t_f$		-	84	-	ns
Turn-off energy	$E_{off}$		-	1.65	-	mJ



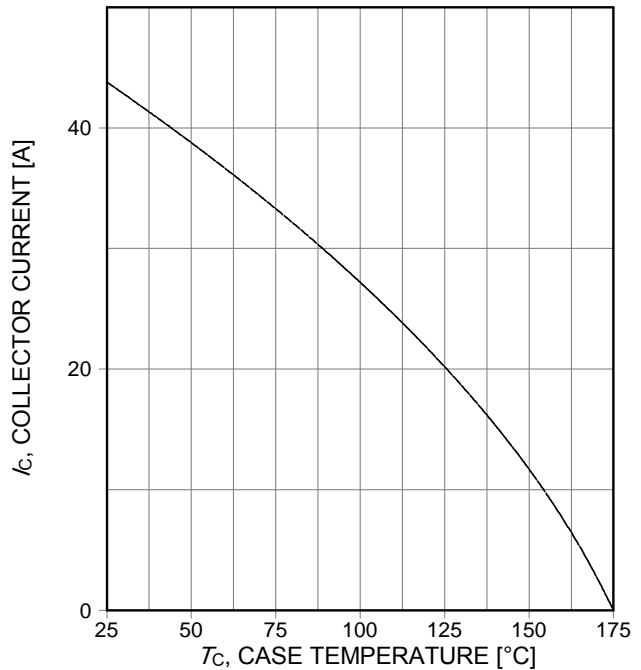
**Figure 1. Collector current as a function of switching frequency**  
 ( $T_j \leq 175^\circ\text{C}$ ,  $D=0.5$ ,  $V_{CE}=600\text{V}$ ,  $V_{GE}=15/0\text{V}$ ,  $R_G=15\Omega$ )



**Figure 2. Forward bias safe operating area**  
 ( $D=0$ ,  $T_C=25^\circ\text{C}$ ,  $T_j \leq 175^\circ\text{C}$ ;  $V_{GE}=15\text{V}$ )



**Figure 3. Power dissipation as a function of case temperature**  
 ( $T_j \leq 175^\circ\text{C}$ )



**Figure 4. Collector current as a function of case temperature**  
 ( $V_{GE} \geq 15\text{V}$ ,  $T_j \leq 175^\circ\text{C}$ )

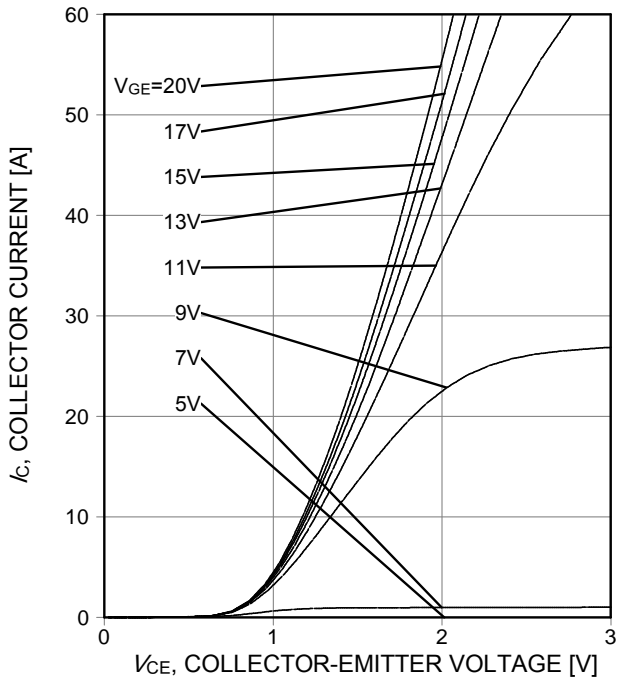


Figure 5. Typical output characteristic ( $T_j=25^\circ\text{C}$ )

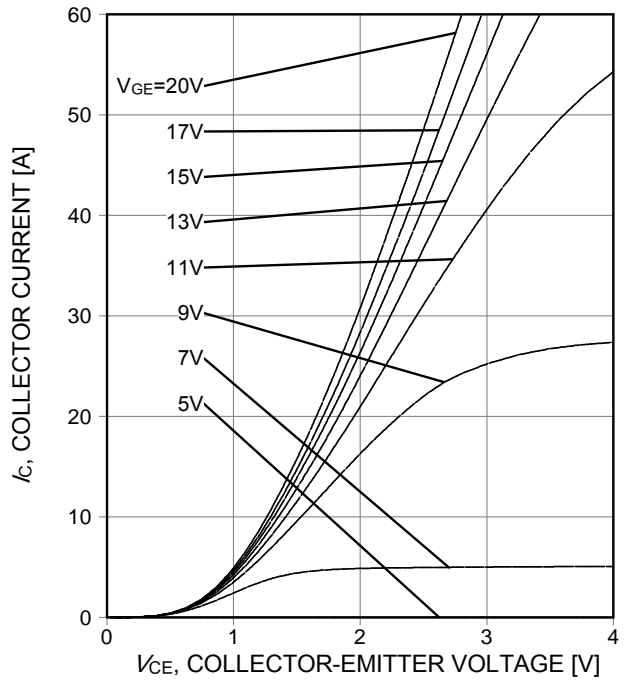


Figure 6. Typical output characteristic ( $T_j=175^\circ\text{C}$ )

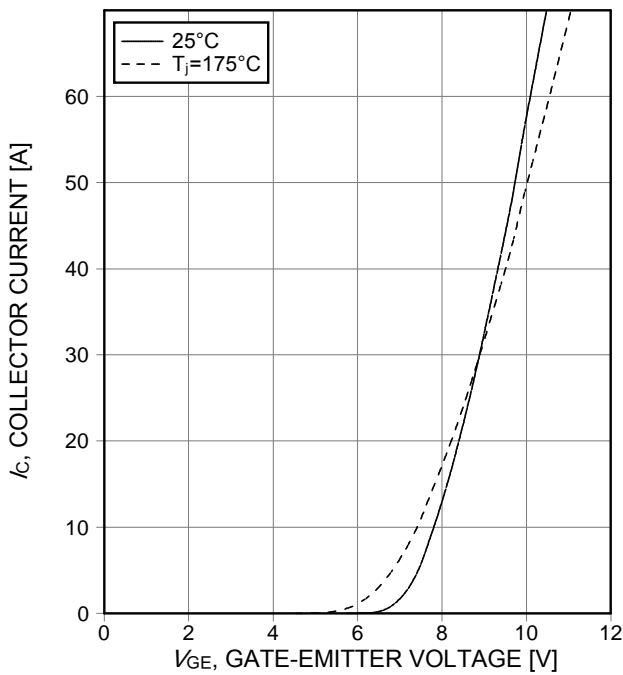


Figure 7. Typical transfer characteristic ( $V_{CE}=20\text{V}$ )

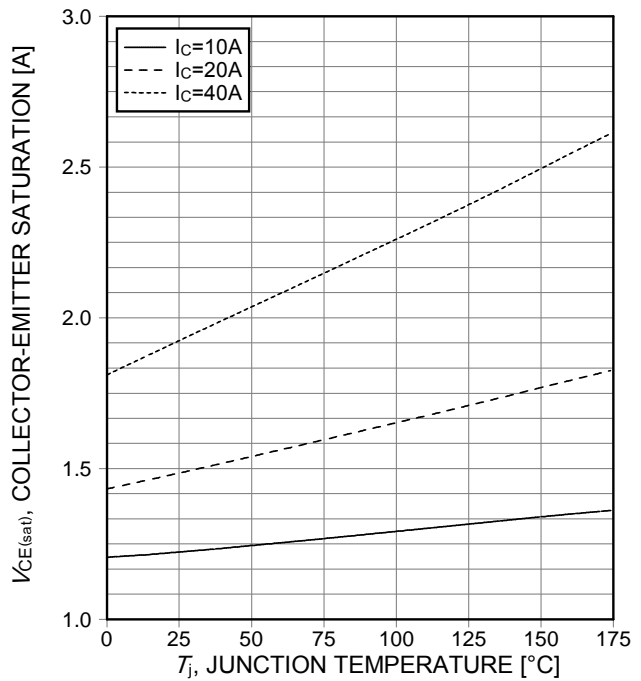
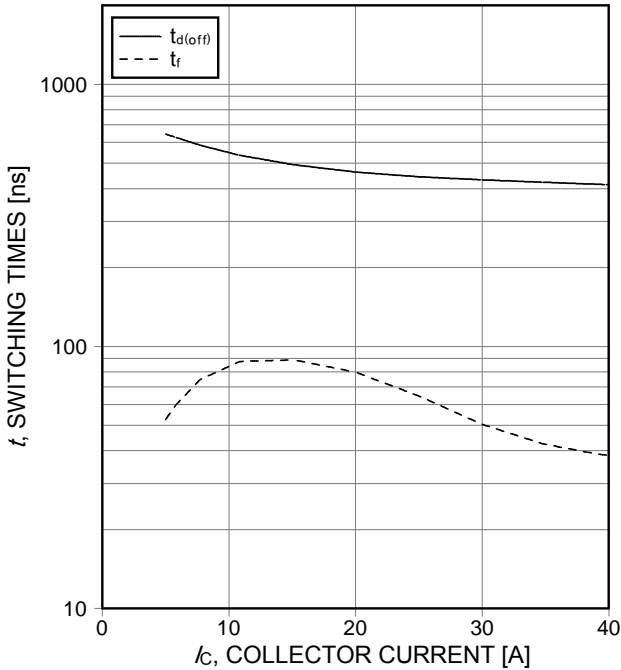
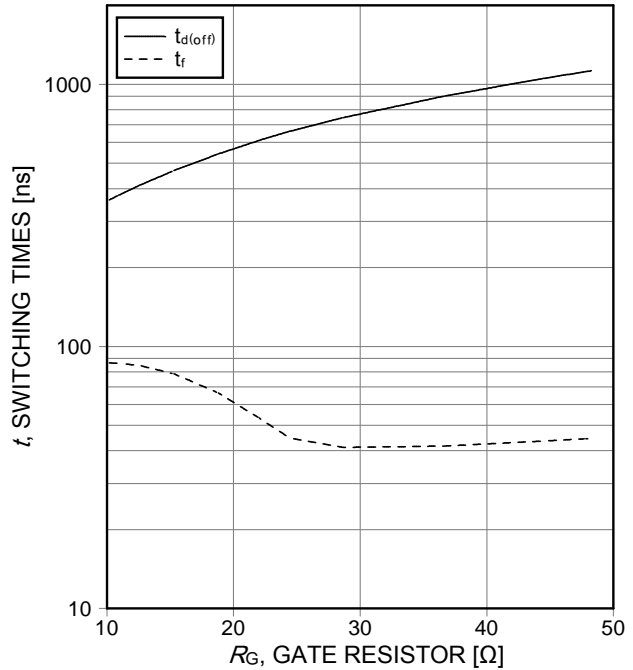


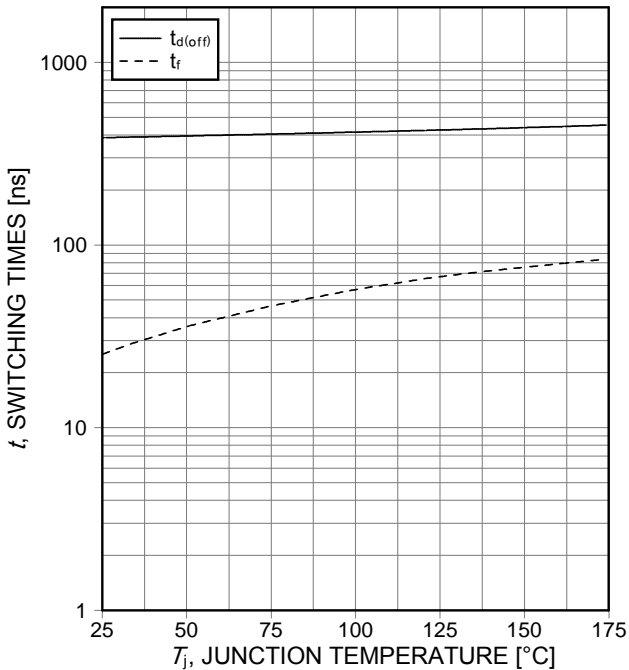
Figure 8. Typical collector-emitter saturation voltage as a function of junction temperature ( $V_{GE}=15\text{V}$ )



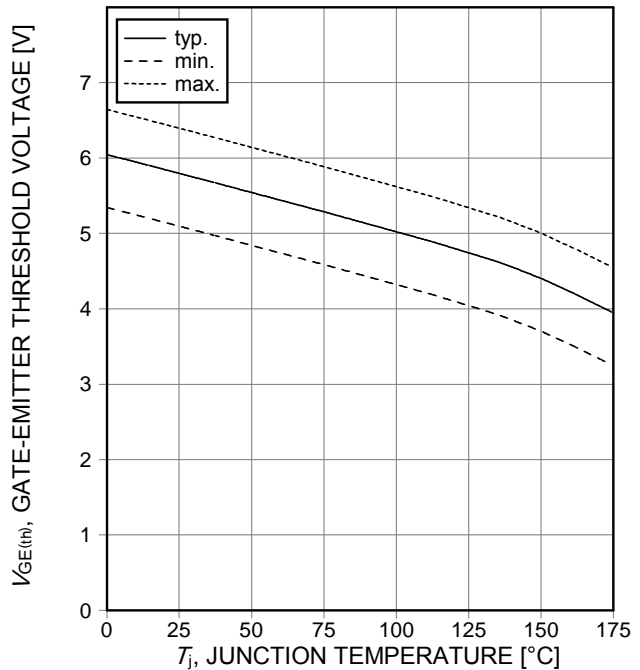
**Figure 9. Typical switching times as a function of collector current**  
 (ind. load,  $T_j=175^\circ\text{C}$ ,  $V_{CE}=600\text{V}$ ,  $V_{GE}=15/0\text{V}$ ,  $R_G=15\Omega$ , test circuit in Fig. E)



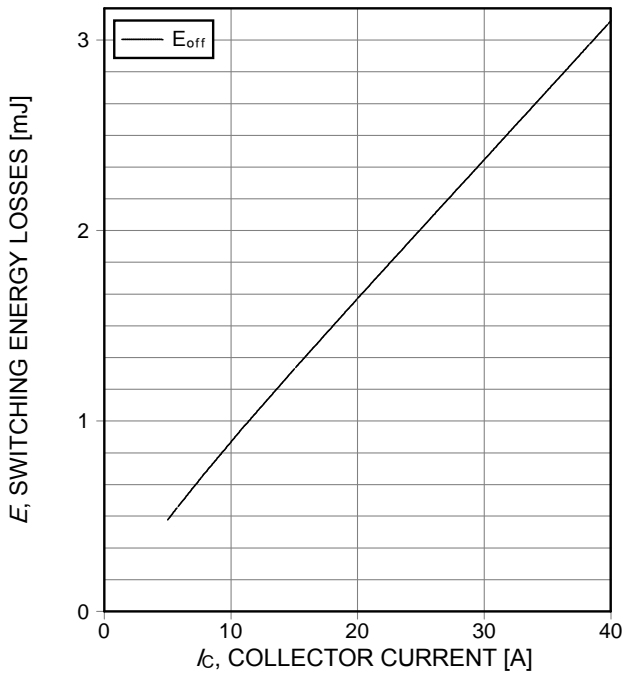
**Figure 10. Typical switching times as a function of gate resistor**  
 (ind. load,  $T_j=175^\circ\text{C}$ ,  $V_{CE}=600\text{V}$ ,  $V_{GE}=15/0\text{V}$ ,  $I_C=20\text{A}$ , test circuit in Fig. E)



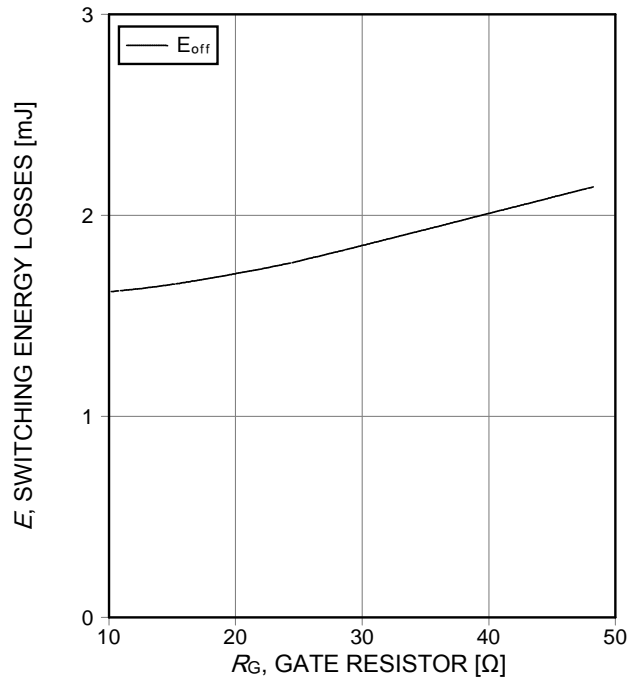
**Figure 11. Typical switching times as a function of junction temperature**  
 (ind. load,  $V_{CE}=600\text{V}$ ,  $V_{GE}=15/0\text{V}$ ,  $I_C=20\text{A}$ ,  $R_G=15\Omega$ , test circuit in Fig. E)



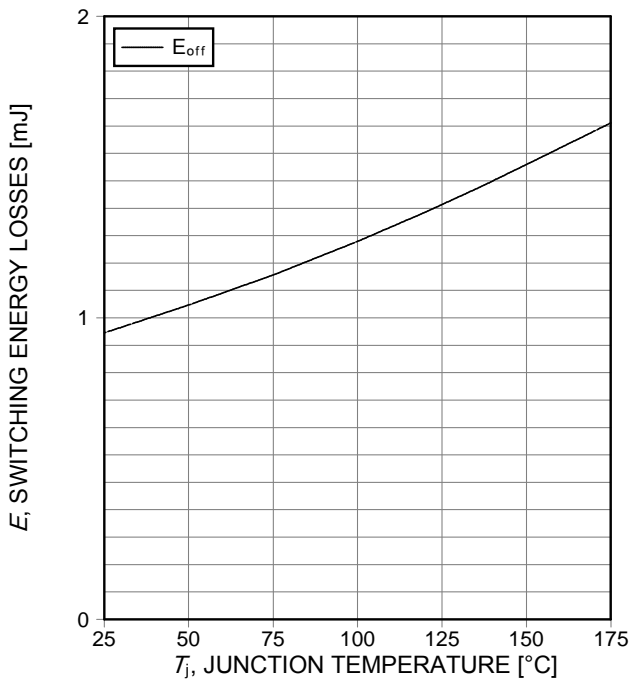
**Figure 12. Gate-emitter threshold voltage as a function of junction temperature**  
 ( $I_C=0.5\text{mA}$ )



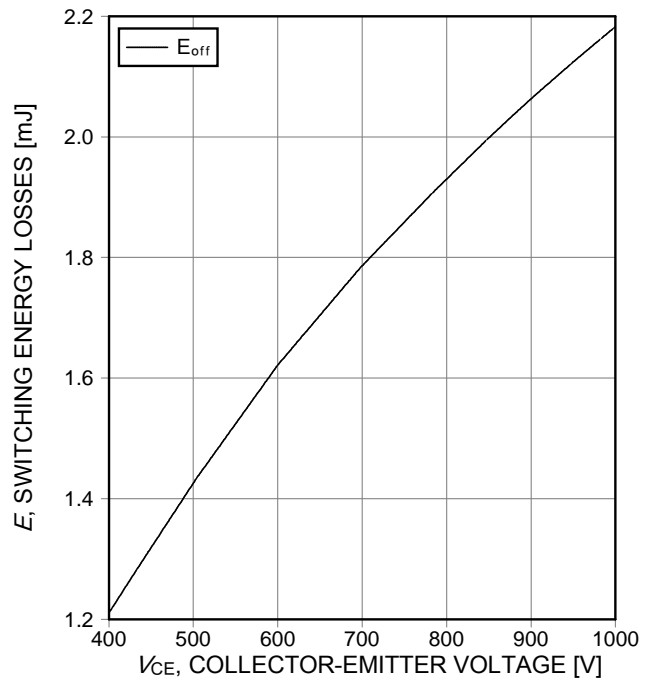
**Figure 13. Typical switching energy losses as a function of collector current**  
 (ind. load,  $T_j=175^\circ\text{C}$ ,  $V_{CE}=600\text{V}$ ,  $V_{GE}=15/0\text{V}$ ,  $R_G=15\Omega$ , test circuit in Fig. E)



**Figure 14. Typical switching energy losses as a function of gate resistor**  
 (ind. load,  $T_j=175^\circ\text{C}$ ,  $V_{CE}=600\text{V}$ ,  $V_{GE}=15/0\text{V}$ , test circuit in Fig. E)



**Figure 15. Typical switching energy losses as a function of junction temperature**  
 (ind. load,  $V_{CE}=600\text{V}$ ,  $V_{GE}=15/0\text{V}$ ,  $I_c=20\text{A}$ ,  $R_G=15\Omega$ , test circuit in Fig. E)



**Figure 16. Typical switching energy losses as a function of collector emitter voltage**  
 (ind. load,  $T_j=175^\circ\text{C}$ ,  $V_{GE}=15/0\text{V}$ ,  $I_c=20\text{A}$ ,  $R_G=15\Omega$ , test circuit in Fig. E)

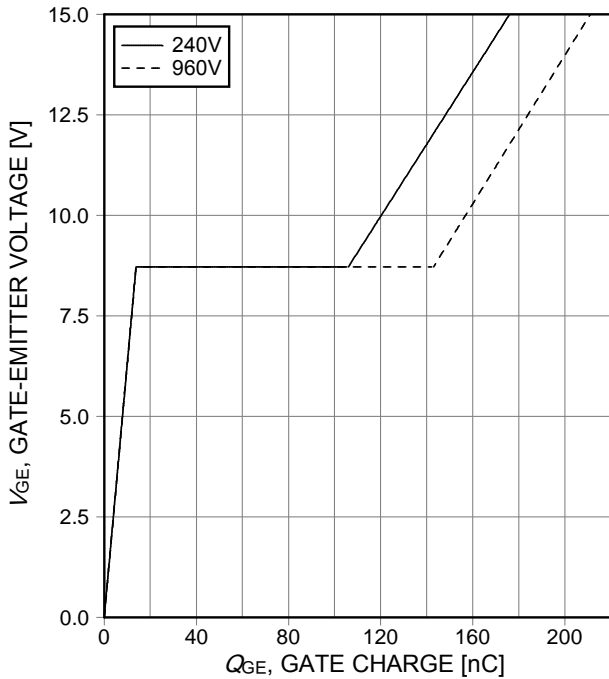


Figure 17. Typical gate charge  
( $I_C=20A$ )

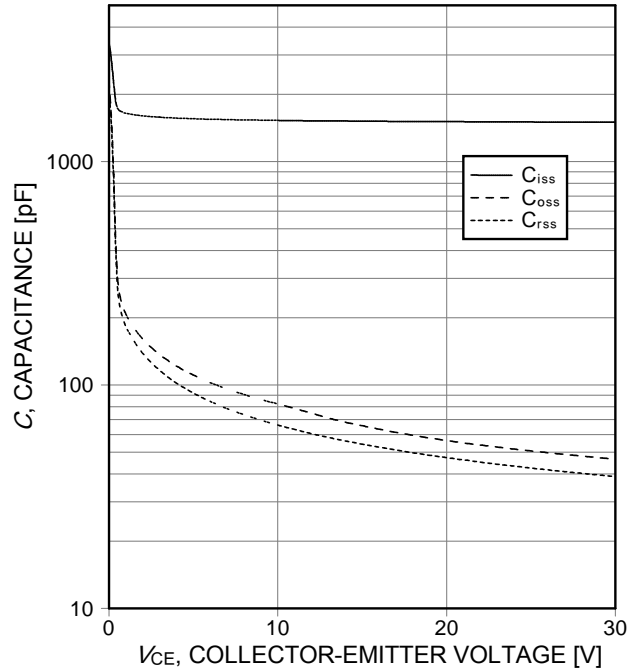


Figure 18. Typical capacitance as a function of collector-emitter voltage  
( $V_{GE}=0V$ ,  $f=1MHz$ )

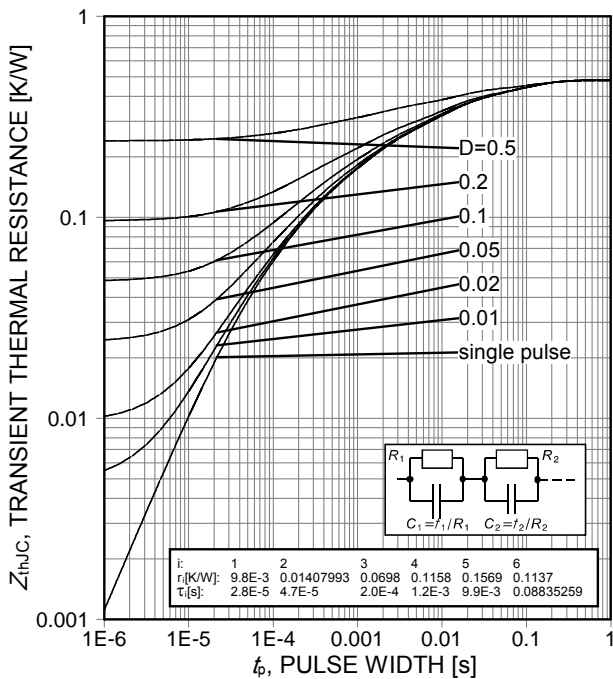


Figure 19. IGBT transient thermal resistance  
( $D=tp/T$ )

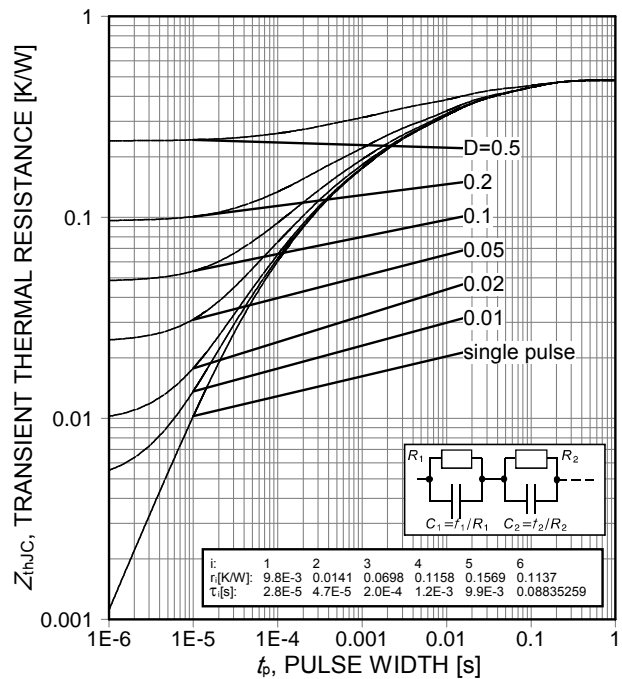


Figure 20. Diode transient thermal impedance as a function of pulse width  
( $D=tp/T$ )



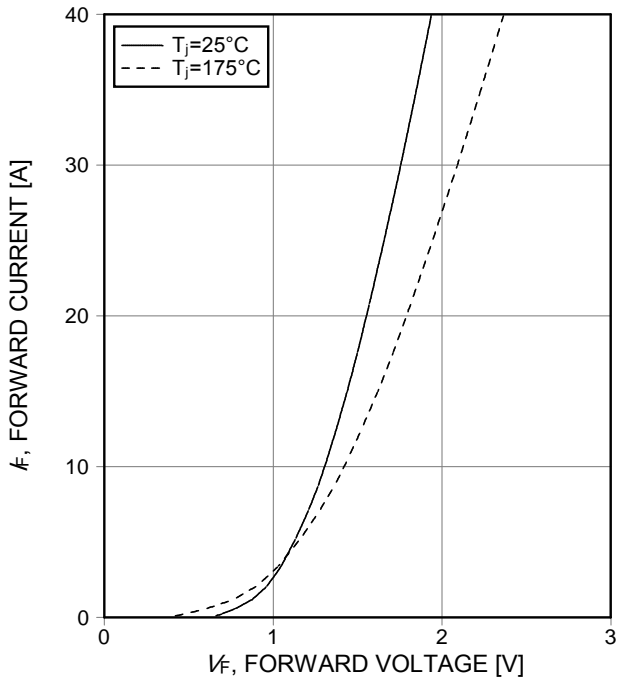


Figure 21. Typical diode forward current as a function of forward voltage

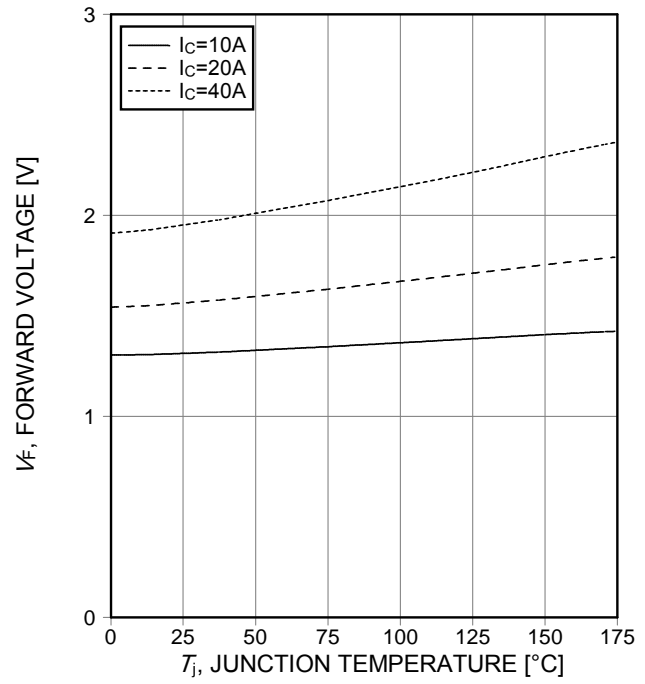
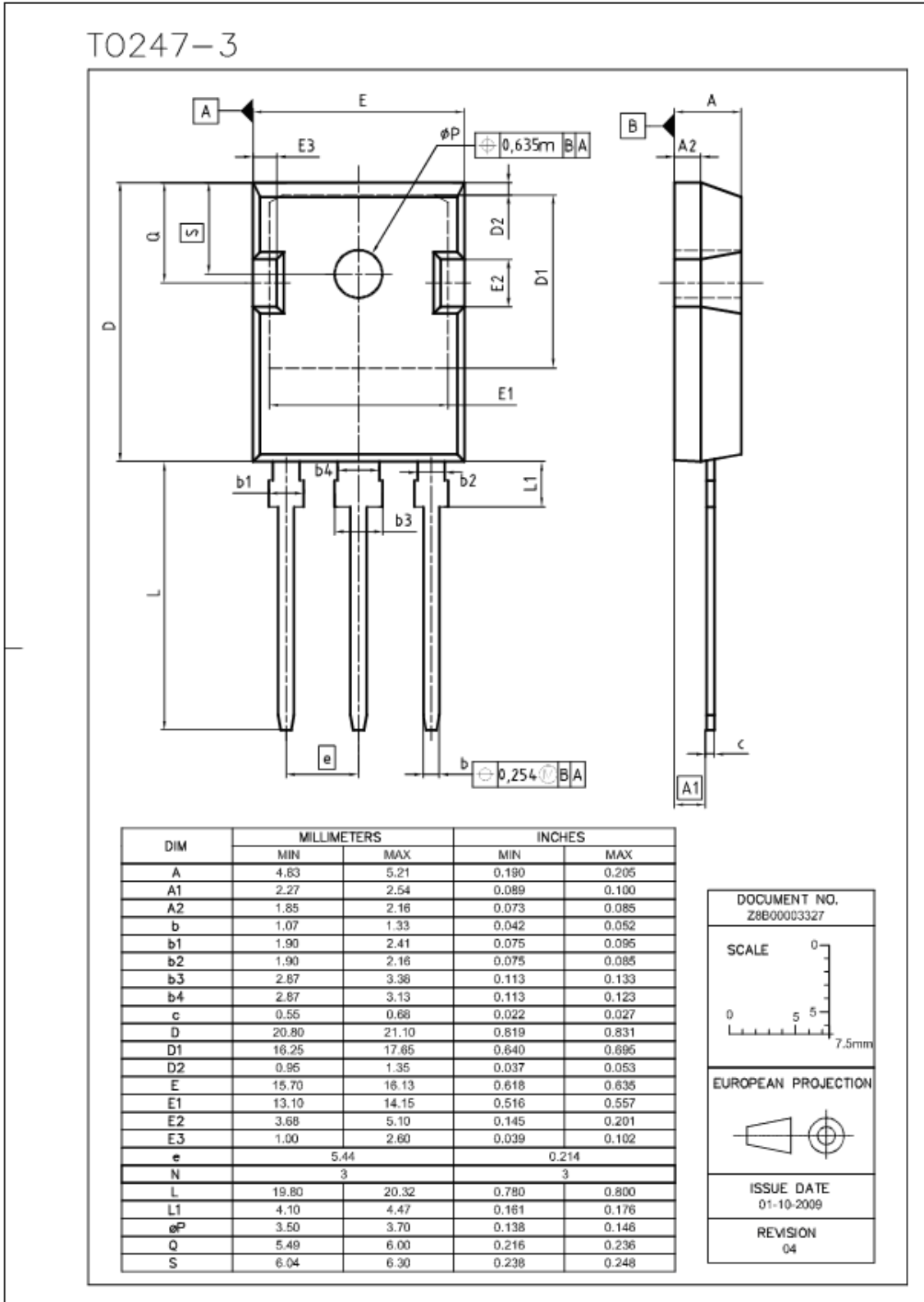


Figure 22. Typical diode forward voltage as a function of junction temperature

PG-TO247-3



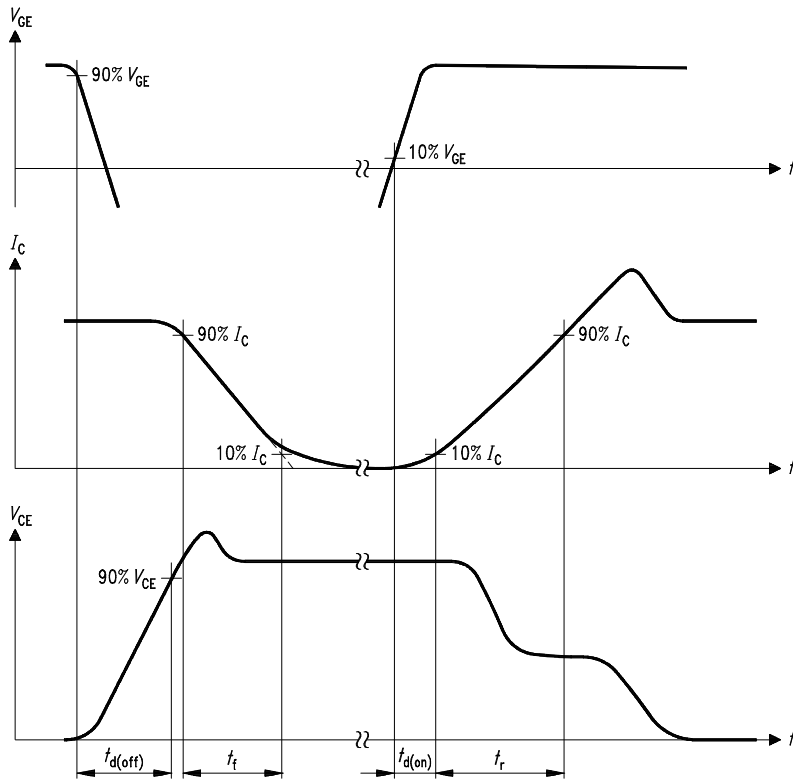


Figure A. Definition of switching times

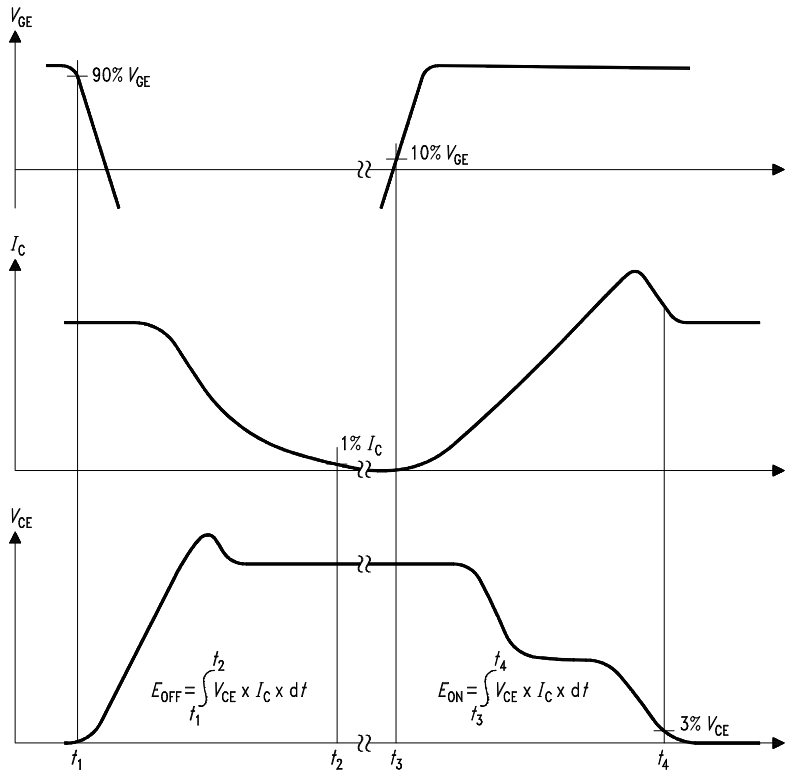


Figure B. Definition of switching losses

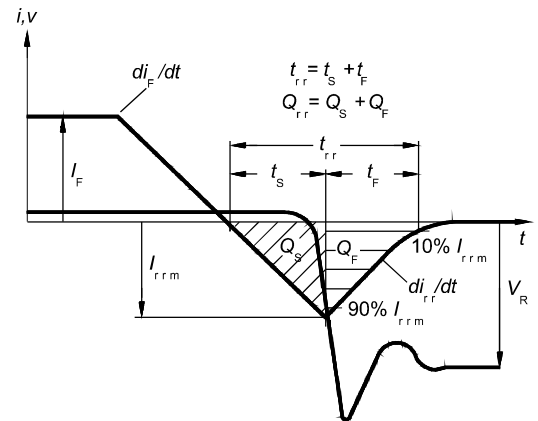


Figure C. Definition of diodes switching characteristics

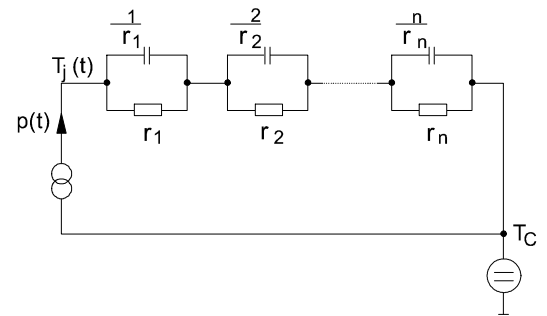


Figure D. Thermal equivalent circuit

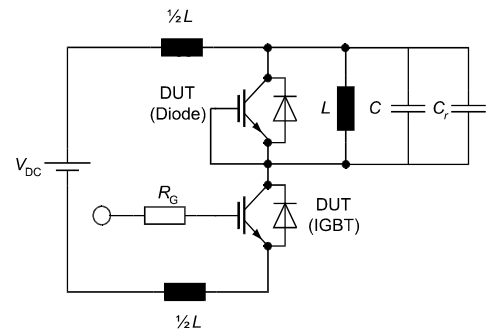


Figure E. Dynamic test circuit  
 Leakage inductance  $L = 180\text{nH}$ ,  
 Stray capacitor  $C_s = 40\text{pF}$ ,  
 Relief capacitor  $C_r = 1\text{nF}$   
 (only for ZVT switching)

**Published by**  
**Infineon Technologies AG**  
**81726 Munich, Germany**  
**81726 München, Germany**  
**© 2009 Infineon Technologies AG**  
**All Rights Reserved.**

**Legal Disclaimer**

The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics. With respect to any examples or hints given herein, any typical values stated herein and/or any information regarding the application of the device, Infineon Technologies hereby disclaims any and all warranties and liabilities of any kind, including without limitation, warranties of non-infringement of intellectual property rights of any third party.

**Information**

For further information on technology, delivery terms and conditions and prices, please contact the nearest Infineon Technologies Office ([www.infineon.com](http://www.infineon.com)).

**Warnings**

Due to technical requirements, components may contain dangerous substances. For information on the types in question, please contact the nearest Infineon Technologies Office. Infineon Technologies components may be used in life-support devices or systems only with the express written approval of Infineon Technologies, if a failure of such components can reasonably be expected to cause the failure of that life-support device or system or to affect the safety or effectiveness of that device or system. Life support devices or systems are intended to be implanted in the human body or to support and/or maintain and sustain and/or protect human life. If they fail, it is reasonable to assume that the health of the user or other persons may be endangered.