

Low Quiescent Current Low Dropout

MD71XXH Series

High Input (30V) CMOS Voltage Regulator

30mA

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The MD71XXH series is a family of Low Dropout Positive regulators, which developed by using CMOS technology. These IC products perform with high output voltage accuracy, low quiescent current, Low Input-Output Dropout as built-in low on-resistance. At the same time with high input voltage capacity it can be up to 30V, suitable for applications which requires higher voltage circuit.

**■ Feature (Typical):**

- high output voltage accuracy  $\pm 3\%$
- low dropout voltage 40mV
- low quiescent current 1.2uA
- low output voltage temperature drift 50 PPM / $^{\circ}\text{C}$
- high input voltage up to 30V keeping stable output voltage
- Output short circuit protection Short-circuit current less than 100 mA

**■ Applications**

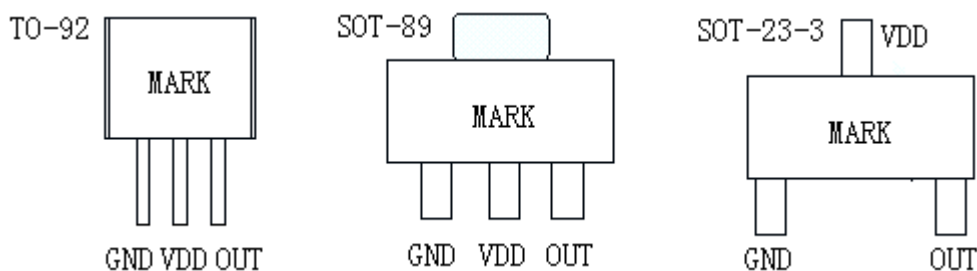
- battery-powered devices
- mobile phone devices
- communications equipments
- home electric/toy appliances
- portable medical equipments

**■ Selection table**

Part NO.	Output voltage (Note)	Tolerance
MD7130	3.0V	$\pm 3\%$
MD7133	3.3V	$\pm 3\%$
MD7136	3.6V	$\pm 3\%$
MD7144	4.4V	$\pm 3\%$
MD7150	5.0V	$\pm 3\%$

NOTE: for semi\_custom parts selectable output voltage from 1.5~7.0V in 0.1V Increment.

■ Package and Pin Assignment



■ Absolute Maximum Ratings: (Ta=25°C unless otherwise specified)

Item	Symbol	Absolute Maximum Ratings	Unit
Input voltage	V <sub>IN</sub>	30	V
Output voltage	V <sub>OUT</sub>	V <sub>SS</sub> -0.3 ~ V <sub>IN</sub> +0.3	
Power dissipation	P <sub>D</sub>	SOT_89 500 TO_92 300 SOT23 250	mW
Operation temperature range	T <sub>opr</sub>	-40~+85	
Storage temperature range	T <sub>stg</sub>	-40~+125	°C

**Note:** The absolute maximum ratings are rated values exceeding which the product could suffer physical damage. These values must therefore not be exceeded under any conditions.

■ Electrical Characteristics:

MD71XXH series (MD7130H, Output voltage +3.0V) (Ta=25°C unless otherwise specified)

ITEM	SYMBOL	CONDITIONS	MIN	TYPE	MAX	UNITS	Test circuits
Output voltage	V <sub>OUT</sub>	V <sub>IN</sub> = 5V, I <sub>OUT</sub> =10mA	2.91	3.0	3.09	V	1
Output current *1	I <sub>OUT</sub>	V <sub>IN</sub> = 5V	25		30	mA	3
Dropout voltage *2	V <sub>drop</sub>	I <sub>OUT</sub> =1 mA		40	60	mV	1
Line regulation	$\frac{\Delta V_{OUT1}}{\Delta V_{IN} \cdot V_{OUT}}$	4V ≤ V <sub>IN</sub> ≤ 30V I <sub>OUT</sub> =1mA		0.05	0.2	%/V	
Load regulation	$\Delta V_{OUT2}$	V <sub>IN</sub> =5V 1.0mA ≤ I <sub>OUT</sub> ≤ 30mA		60	100	mV	
Temperature coefficient	$\frac{\Delta V_{OUT}}{\Delta T_a \cdot V_{OUT}}$	V <sub>IN</sub> =5V, I <sub>OUT</sub> =10mA -40°C ≤ T <sub>a</sub> ≤ 85°C		± 50	± 100	Ppm/°C	
Current consumption	I <sub>SS1</sub>	V <sub>IN</sub> =5V 无负载		1.2	3.0	uA	2
Input voltage	V <sub>IN</sub>	--			30	V	

MD71XXH series(MD7133H, Output voltage +3.3V) (Ta=25°C unless otherwise specified)

ITEM	SYMBOL	CONDITIONS	MIN	TYPE	MAX	UNITS	Test circuits
Output voltage	V <sub>OUT</sub>	V <sub>IN</sub> = 5.3V, I <sub>OUT</sub> =10mA	3.201	3.3	3.399	V	1
Output current *1	I <sub>OUT</sub>	V <sub>IN</sub> = 5.3V	25		30	mA	3
Dropout voltage *2	V <sub>drop</sub>	I <sub>OUT</sub> =1 mA I <sub>OUT</sub> =10 mA		40	60	mV	1
Line regulation	$\frac{\Delta V_{OUT1}}{\Delta V_{IN} \cdot V_{OUT}}$	4.3V ≤ V <sub>IN</sub> ≤ 24V I <sub>OUT</sub> =1mA		0.05	0.2	%/V	
Load regulation	ΔV <sub>OUT2</sub>	V <sub>IN</sub> =5.3V 1.0mA ≤ I <sub>OUT</sub> ≤ 100mA		60	100	mV	
Temperature coefficient	$\frac{\Delta V_{OUT}}{\Delta T_a \cdot V_{OUT}}$	V <sub>IN</sub> =5.3V, I <sub>OUT</sub> =10mA -40°C ≤ T <sub>a</sub> ≤ 85°C		± 50	± 100	ppm/°C	
Current consumption	I <sub>SS1</sub>	V <sub>IN</sub> =24V 无负载		1.2	3.0	uA	2
Input voltage	V <sub>IN</sub>	--			30	V	

MD71XXH series(MD7136H, Output voltage +3.6V) (Ta=25°C unless otherwise specified)

ITEM	SYMBOL	CONDITIONS	MIN	TYPE	MAX	UNITS	Test circuits
Output voltage	V <sub>OUT</sub>	V <sub>IN</sub> = 5.6V, I <sub>OUT</sub> =10mA	3.492	3.6	3.708	V	1
Output current *1	I <sub>OUT</sub>	V <sub>IN</sub> = 5.6V	25		30	mA	3
Dropout voltage *2	V <sub>drop</sub>	I <sub>OUT</sub> =1 mA I <sub>OUT</sub> =10mA		40	60	mV	1
Line regulation	$\frac{\Delta V_{OUT1}}{\Delta V_{IN} \cdot V_{OUT}}$	4.6V ≤ V <sub>IN</sub> ≤ 24V I <sub>OUT</sub> =1mA		0.05	0.2	%/V	
Load regulation	ΔV <sub>OUT2</sub>	V <sub>IN</sub> =5.6V 1.0mA ≤ I <sub>OUT</sub> ≤ 100mA		60	100	mV	
Temperature coefficient	$\frac{\Delta V_{OUT}}{\Delta T_a \cdot V_{OUT}}$	V <sub>IN</sub> =5.6V, I <sub>OUT</sub> =10mA -40°C ≤ T <sub>a</sub> ≤ 85°C		± 50	± 100	ppm/°C	
Current consumption	I <sub>SS1</sub>	V <sub>IN</sub> =24V 无负载		1.2	3.0	uA	2
Input voltage	V <sub>IN</sub>	--			30	V	

MD71XXH series(MD7144H, Output voltage +4.4V) (Ta=25°C unless otherwise specified)

ITEM	SYMBOL	CONDITIONS	MIN	TYPE	MAX	UNITS	Test circuits
Output voltage	V <sub>OUT</sub>	V <sub>IN</sub> = 6.4V, I <sub>OUT</sub> =10mA	4.268	4.4	4.532	V	1
Output current *1	I <sub>OUT</sub>	V <sub>IN</sub> = 6.4V	25		30	mA	3
Dropout voltage *2	V <sub>drop</sub>	I <sub>OUT</sub> =1 mA		40	60	mV	1
Line regulation	$\frac{\Delta V_{OUT1}}{\Delta V_{IN} \cdot V_{OUT}}$	5.4V ≤ V <sub>IN</sub> ≤ 30V I <sub>OUT</sub> =1mA		0.05	0.2	%/V	
Load regulation	ΔV <sub>OUT2</sub>	V <sub>IN</sub> =6.4V 1.0mA ≤ I <sub>OUT</sub> ≤ 30mA		60	100	mV	
Temperature coefficient	$\frac{\Delta V_{OUT}}{\Delta T_a \cdot V_{OUT}}$	V <sub>IN</sub> =6.4V, I <sub>OUT</sub> =10mA -40°C ≤ T <sub>a</sub> ≤ 85°C		± 50	± 100	ppm/°C	
Current consumption	I <sub>SS1</sub>	V <sub>IN</sub> =6.4V 无负载		1.2	3.0	uA	2
Input voltage	V <sub>IN</sub>	--			30	V	

MD71XXH series(MD7150H, Output voltage +5.0V) (Ta=25°C unless otherwise specified)

ITEM	SYMBOL	CONDITIONS	MIN	TYPE	MAX	UNITS	Test circuits
Output voltage	V <sub>OUT</sub>	V <sub>IN</sub> = 7V, I <sub>OUT</sub> =10mA	4.85	5.0	5.15	V	1
Output current *1	I <sub>OUT</sub>	V <sub>IN</sub> = 7V	25		30	mA	3
Dropout voltage *2	V <sub>drop</sub>	I <sub>OUT</sub> =1 mA		40	60	mV	1
Line regulation	$\frac{\Delta V_{OUT1}}{\Delta V_{IN} \cdot V_{OUT}}$	6V ≤ V <sub>IN</sub> ≤ 30V I <sub>OUT</sub> =1mA		0.05	0.2	%/V	
Load regulation	ΔV <sub>OUT2</sub>	V <sub>IN</sub> =7V 1.0mA ≤ I <sub>OUT</sub> ≤ 30mA		60	100	mV	
Temperature coefficient	$\frac{\Delta V_{OUT}}{\Delta T_a \cdot V_{OUT}}$	V <sub>IN</sub> =7V, I <sub>OUT</sub> =10mA -40°C ≤ T <sub>a</sub> ≤ 85°C		± 50	± 100	ppm/°C	
Current consumption	I <sub>SS1</sub>	V <sub>IN</sub> =7V 无负载		1.2	3.0	uA	2
Input voltage	V <sub>IN</sub>	--			30	V	

\* 1. Increasing output current slowly, The I<sub>OUT</sub> when output voltage decreasing two percent

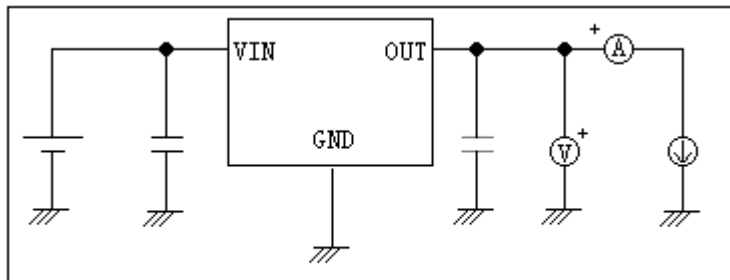
\* 2.  $V_{drop} = V_{IN1} - (V_{OUT(E)} \times 0.98V)$

V<sub>OUT(E)</sub> Effective output voltage, i.e., the output voltage when fixing I<sub>OUT</sub>(=1mA) and inputting V<sub>OUT(S)</sub>+2.0 V.

V<sub>IN1</sub>: is the Input voltage at which output voltage becomes 98 percent of V<sub>OUT(E)</sub> after gradually decreasing input voltage.

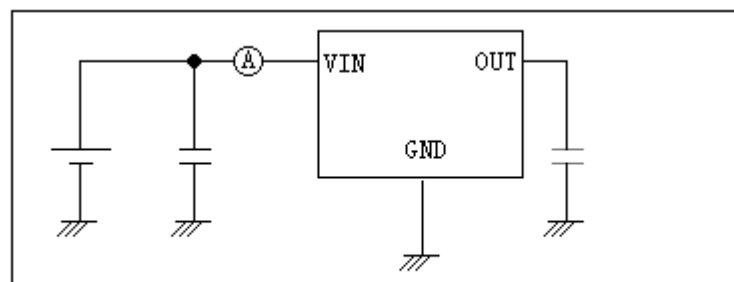
## ■ Test Circuits

1.



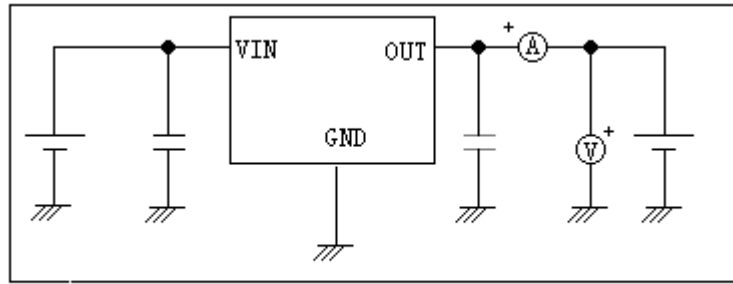
Circuit 1

2.



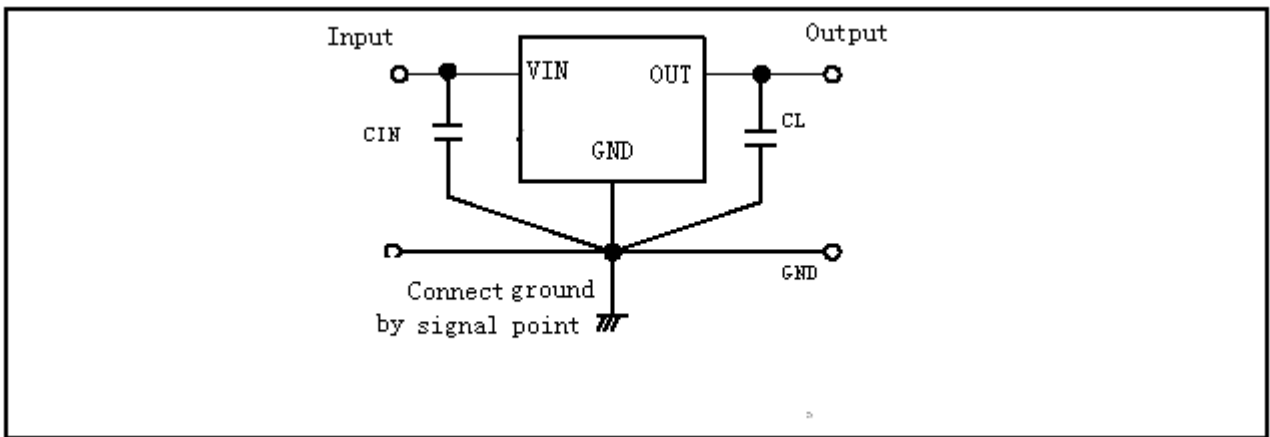
Circuit 2

3.



Circuit 3

### ■Standard Circuit:



Note: The above connection diagram and constant will not guarantee successful operation. Perform through evaluation using the actual application to set the constant.

### ■using conditions:

Input capacitors(CIN):  $>1.0 \mu\text{F}$ .

Output capacitors(CL):  $>2.2 \mu\text{F}$ (tantalum capacitors).

\*1.  $C_{IN}$  is a capacitor for stabilizing the input.

\*2. A ceramic capacitor can be used for  $C_L$  besides a tantalum capacitor.

\* Note: In general, linear regulated power supply may lead to oscillation by choosing the different external components. Please make sure the capacitor before used in the application circuit does not oscillate.

### ■Technical Terms

#### 1. Output capacitors ( $C_L$ )

Output capacitors are generally used to stabilize regulation operation and to improve transient response characteristics. But the MD71XXH series can provide stable operation without output capacitors. Capacitors are used only to improve transient response characteristics. Output capacitors can hence be removed in applications in which transient response can be negligible. When an output capacitor is used, a low ESR (Equivalent Series Resistance) capacitor like ceramic capacitor can also be used.

## 2. Output voltage ( $V_{OUT}$ )

The accuracy of the output voltage is  $\pm 3.0\%$  guaranteed under the specified conditions for input voltage, which differs depending upon the product items, output current, and temperature.

**Caution:** If the above conditions change, the output voltage value may vary and go out of the accuracy range of the output voltage. See the electrical characteristics and characteristics data for details.

## 3. Line regulations ( $\Delta V_{OUT1} / \Delta V_{IN} * V_{OUT}$ )

These parameters indicate the input voltage dependence on the output voltage. That is, the values show how much the output voltage changes due to a change in the input voltage with the output current remained unchanged.

## 4. Load regulation ( $\Delta V_{OUT2}$ )

This parameter indicates the output current dependence on the output voltage. That is, the value shows how much the output voltage changes due to a change in the output current with the input voltage remained unchanged.

## 5. Dropout voltage ( $V_{drop}$ )

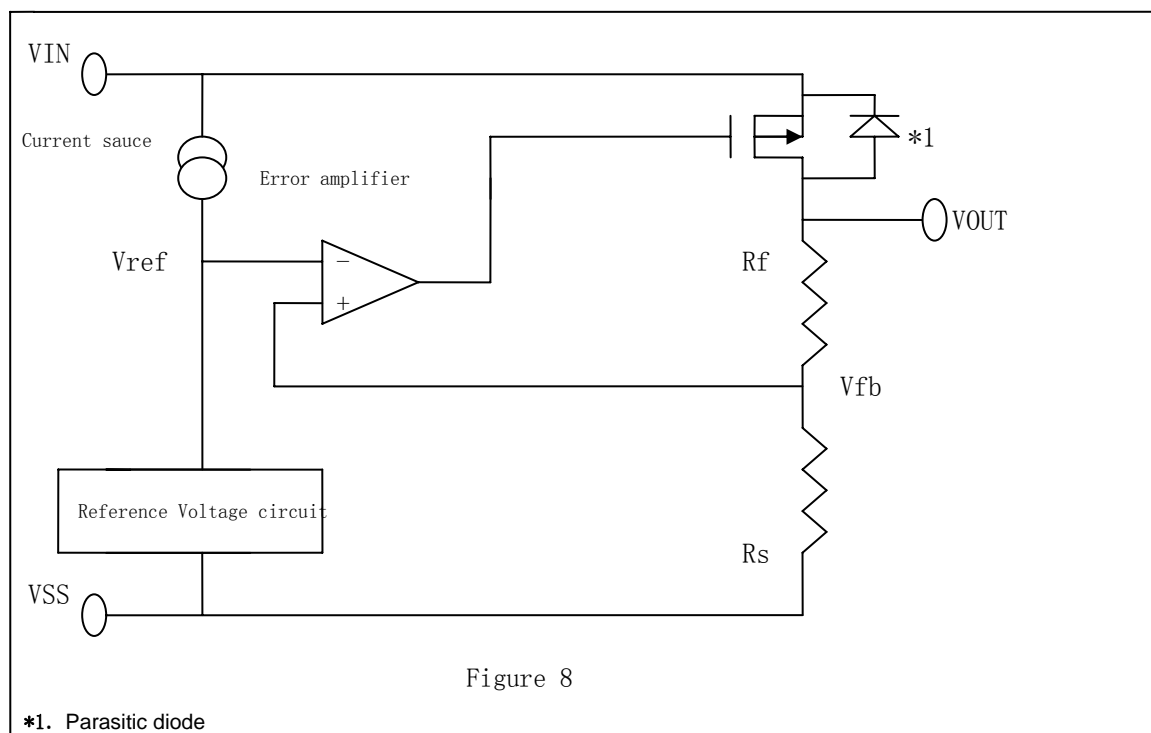
This parameter indicates the difference between the input voltage ( $V_{IN1}$ ) and the output voltage when output voltage falls to 98 % of  $V_{OUT(E)}$  by gradually decreasing the input voltage ( $V_{IN}$ ).

$$V_{drop} = V_{IN1} - [V_{OUT(E)} \times 0.98]$$

## ■ Description of Operation

### 1. Basic Operation

Figure 8 shows the block diagram of the MD71XXH series. The error amplifier compares a reference voltage  $V_{ref}$  with a part of the output voltage divided by the feedback resistors  $R_s$  and  $R_f$ , and supplies the gate voltage to the output transistor, necessary to ensure certain output voltage independent from change of input voltage and temperature.



### 2. Output Transistor

The MD71XXH Series uses a Pch MOS FET as the output transistor.

The voltage at  $V_{OUT}$  must not exceed  $V_{IN}+0.3V$ . When the  $V_{OUT}$  voltage becomes higher than that of  $V_{IN}$ , reverse current flows and may break the regulator since a parasitic diode between  $V_{OUT}$  and  $V_{IN}$  exists inevitably

### 3. Short-circuit protection

Installation of the short-circuit protection which protects the output transistor against short-circuit between  $V_{OUT}$  and  $V_{SS}$  can be selected in the MD71XXH series. The short-circuit protection controls output current as shown in the typical characteristics, (1) OUTPUT VOLTAGE versus OUTPUT CURRENT, and suppresses output current at about 40 mA even if  $V_{OUT}$  and  $V_{SS}$  pins are short-circuited.

The short-circuit protection can not at the same time be a thermal protection. Attention should be paid to the Input voltage and the load current under the actual condition so as not to exceed the power dissipation of the package including the case for short-circuit. When the output current is large and the difference between input and

output voltage is large even if not shorted, the short-circuit protection may work and the output current is suppressed to the specified value. Products without short-circuit protection can provide comparatively large current by removing a short-circuit protection.

## ■ Selection of External Components

### Output Capacitor ( $C_L$ )

The MD71XXH series can provide stable operation without output capacitor ( $C_L$ ) since the regulator has an internal phase compensation circuit to stabilize operation when the load changes. The transient response of the regulator, however, changes with the output capacitor and the magnitude of overshoot and undershoot on output voltage accordingly changes. Please refer to  $C_L$  dependence data in “Transient Response Characteristics” to select suitable value for the capacitor.

When a tantalum or an aluminum electrolytic capacitor is used, the ESR of the capacitor shall be  $10\ \Omega$  or less. When an aluminum electrolytic capacitor is used attention should be especially paid to since the ESR of the aluminum electrolytic capacitor increases at low temperature and possibility of oscillation becomes large. Sufficient evaluation including temperature characteristics is indispensable.

## ■ Precautions:

- Design wiring patterns for VIN, VOUT and GND pins to hold low impedance.

When mounting an output capacitor between the VOUT and VSS pins ( $C_L$ ) and a capacitor for stabilizing the input between VIN and VSS pins ( $C_{IN}$ ), the distance from the capacitor to the VOUT pin and to the VSS pin should be as short as possible.

- Note that output voltage may be increased at low load current of less than 1  $\mu$ A.
- To prevent oscillation, it is recommended to use the external parts under the following conditions.

Equivalent Series Resistance (ESR):  $30\ \Omega$  or less

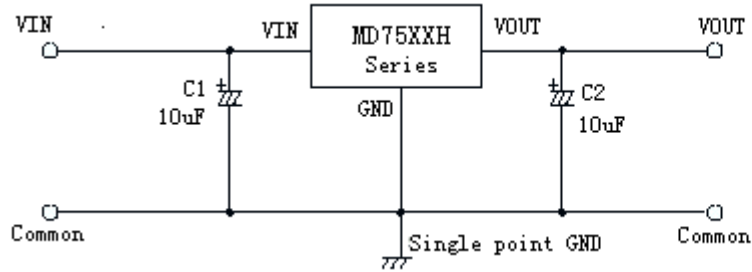
Input series resistance ( $R_{IN}$ ):  $10\ \Omega$  or less

- A voltage regulator may oscillate when power source impedance is high and input capacitor is low or not connected.
- The application condition for input voltage and load current should not exceed the package power dissipation.
- Pay attention to the operating conditions for input/output voltage and load current so that the power loss in the IC does not exceed the power dissipation of the package.
- Do not apply an electrostatic discharge to this IC that exceeds the performance ratings of the built-in electrostatic protection circuit.
- SII claims no responsibility for any and all disputes arising out of or in connection with any infringement of the products including this IC upon patents owned by a third party.

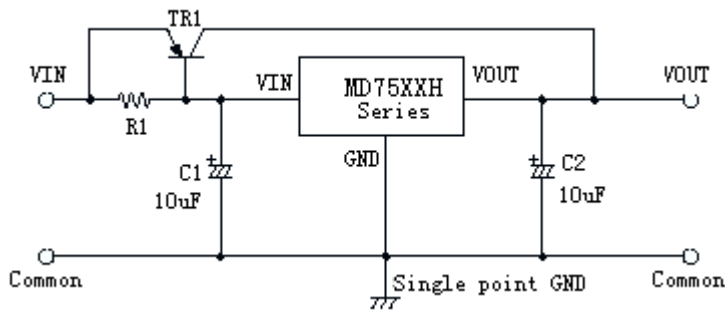


## Application Circuits

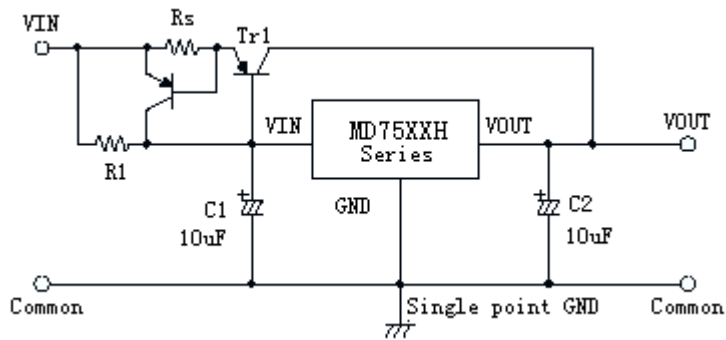
### Basic Circuits



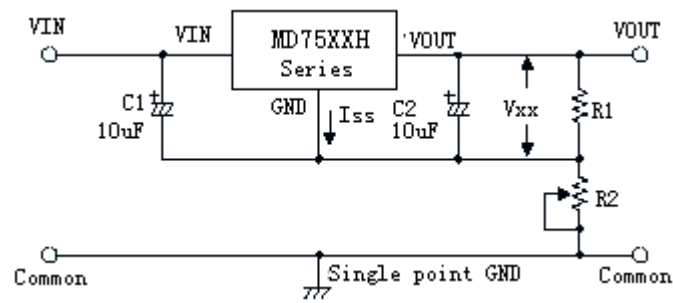
### High Output Current Positive Voltage Regulator



### Short-Circuit Protection by Tr1

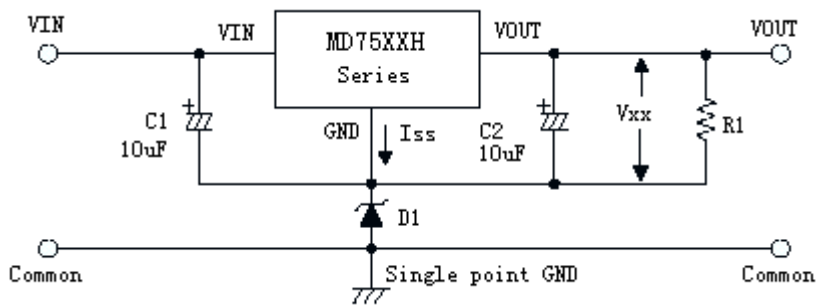


### Circuit for Increasing Output Voltage



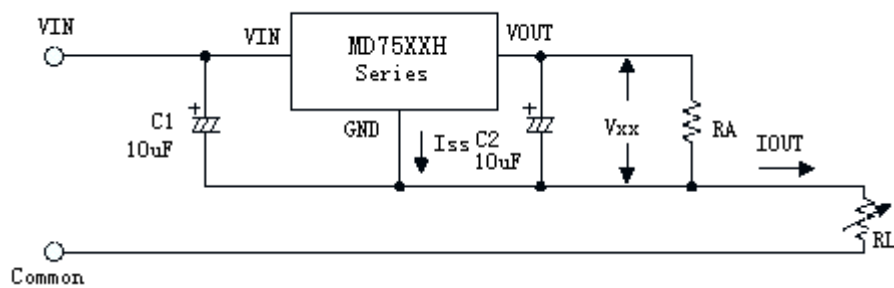
$$V_{OUT} = V_{xx} (1 + R2/R1) + I_{ss} * R2$$

### Constant Current Regulator



$$V_{OUT} = V_{xx} + V_{D1}$$

### Dual Supply



$$I_{OUT} = V_{xx} / R_A + I_{ss}$$

V10.8.20 xF